



PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10001302	FILING DATE 11/01/2001	CLASS 257	SUBCLASS 503	GAU 2815	EXAMINER <i>[Signature]</i>
**APPLICANTS: Zuniga-Ortiz Edgar; Koduri Sreenivasan; <i>S. Christa</i>					
**CONTINUING DATA VERIFIED:					
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** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO TI-33535	
TITLE : Bumpless wafer scale device and board assembly					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Total Claims	Print Claim for O.G.
TERMINAL DISCLAIMER		Sheets Drawg.	Figs. Drawg.
		Print Fig.	
		Application Examiner	
PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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